



MERIT installation: Controls for the 5MW power-supply

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http://cern.ch/merit

September 2006





AB/PO

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MERIT

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Purpose of this meeting

Evaluation of possible layout and work effort

The 5MW power supply in building 193

Controls layout

Estimate of installation work





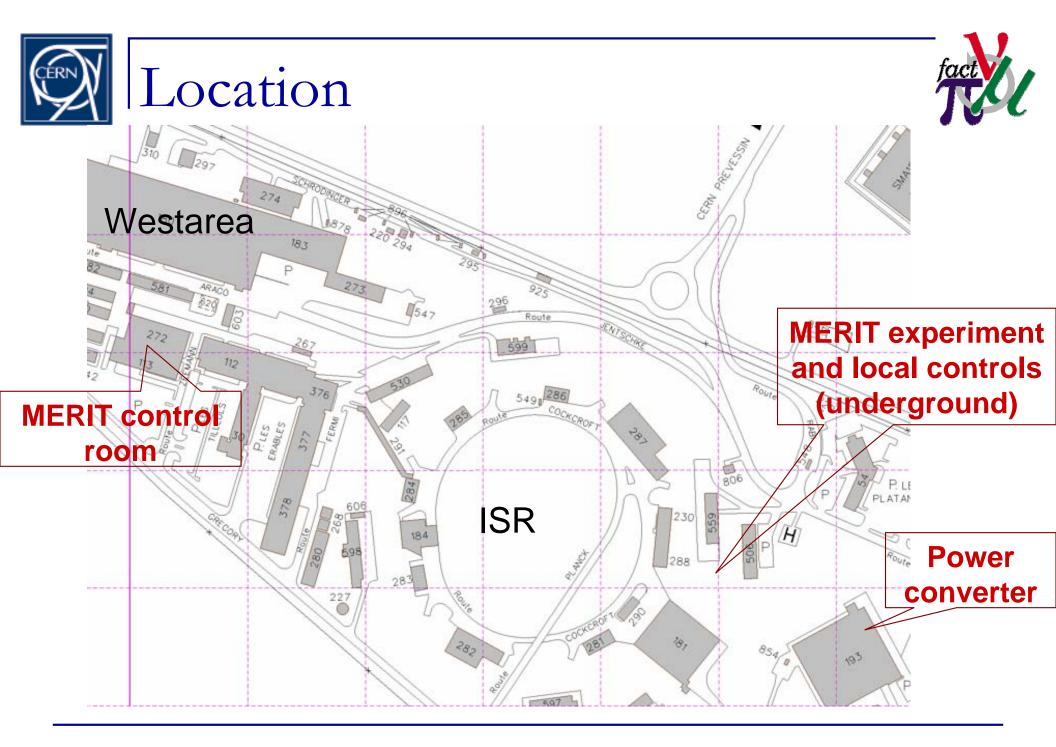
MERIT

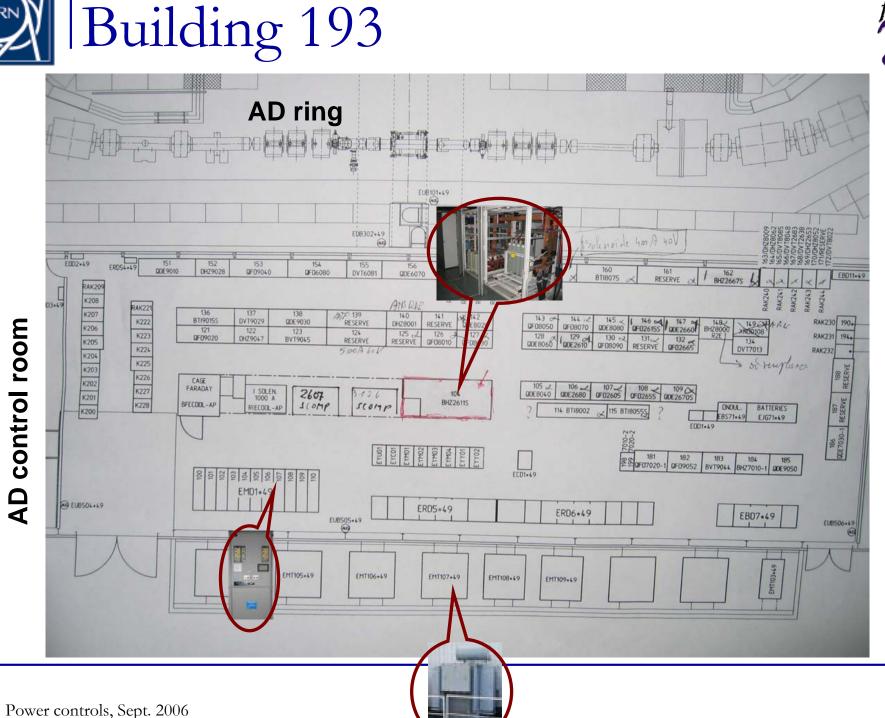
- Single pulse experiment (limited cycles ~500)
- Runtime very limited (a few weeks)

Installation is temporary

- Jan-July 2007 (Run including a commissioning phase)
- Solutions and implementation of controls layout could be based on rental.

Budget (T273180) available

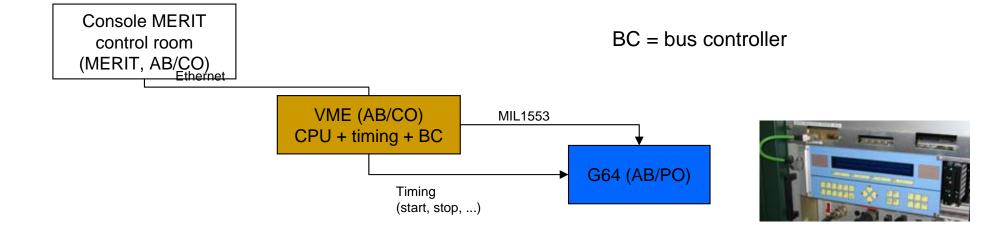




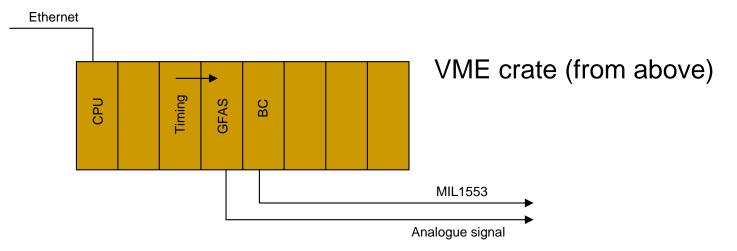




Version 1 (notation Gilles): without GFAS



Version 1 (notation Gilles): with GFAS







- MERIT control room: room 272 S-002
 - Independent from CCC
- Information transmitted to PowerControls
 - Reference current
 - Timing, trigger
- Information read (displayed in MERIT control room):
 - Current reading
 - Status of power supply (Off, ready, interlock, error)





- With or without GFAS?
- Which parts are already available in building 193?
 - VME crate?
 - Cables (MIL1553, timing ...)?
 - Ethernet?
 - **...**
- Software in the MERIT control room
- Where are the interlocks coming into the game?





- Spring/Summer 2006: Installation
 - Power supply (converter + transformer)
 - DC cabling (building 193 to TT2A)
- Autumn 2006: Power converter fully refurbished
- Winter 2007:
 - AC cabling
 - Controls
 - Commissioning of the power supply
- February 2007: MERIT hardware installation
- March-April 2007: MERIT commissioning
- May 2007: ready for MERIT physics run